FIG.1

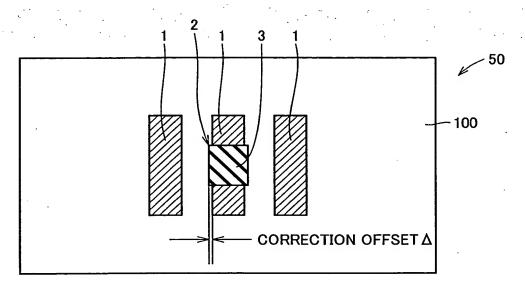


FIG.2

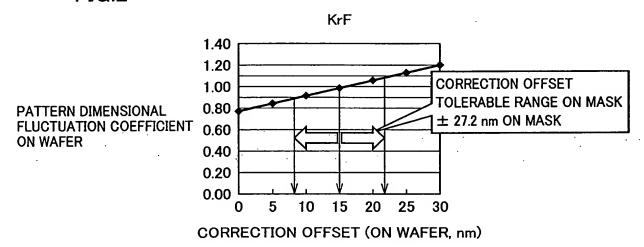


FIG.3

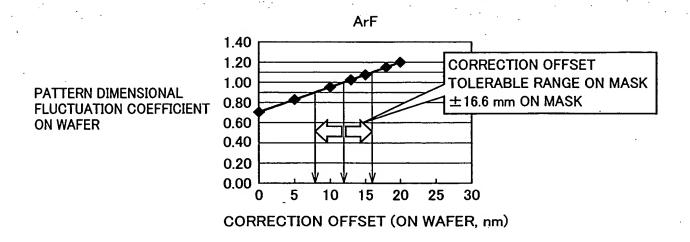
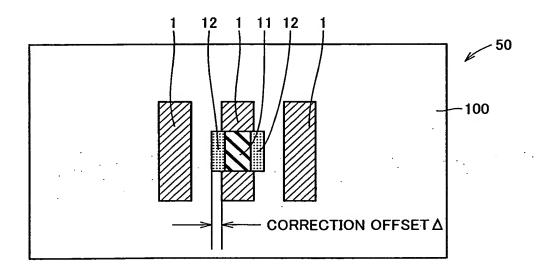


FIG.4



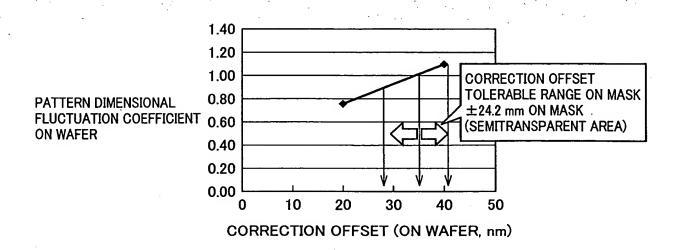


FIG.6

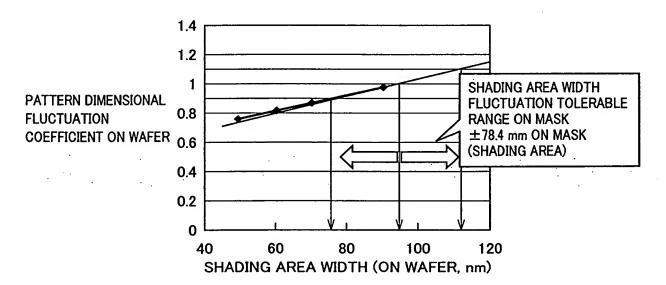
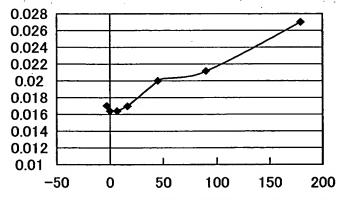


FIG.7

RATIO OF THE DIMENSIONAL FLUCTUATION IN PATTERN ON WAFER TO FLUCTUATION IN SEMITRANSPARENT AREA WIDTH



PHASE DIFFERENCE OF SEMITRANSPARENT AREA (deg)

FIG.8

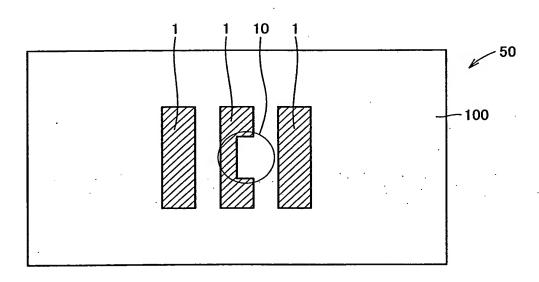


FIG.9

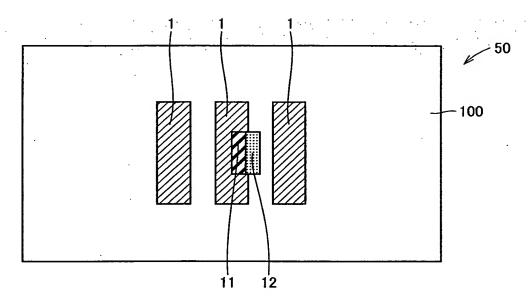


FIG.10

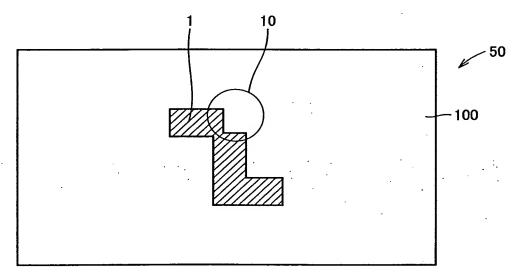


FIG.11

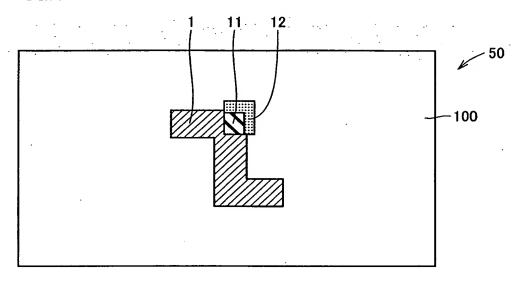


FIG.12

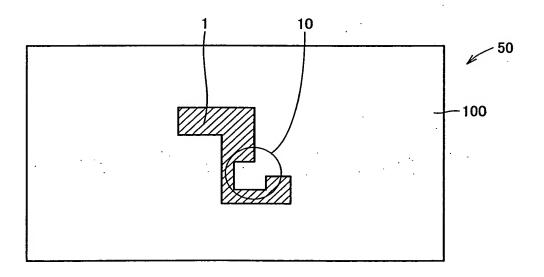


FIG.13

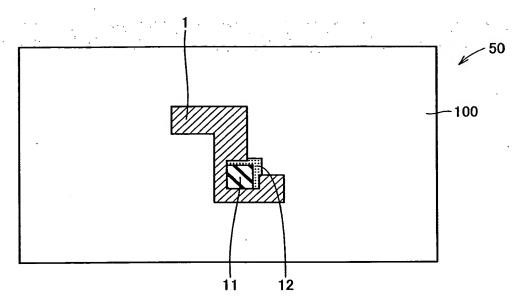


FIG.14

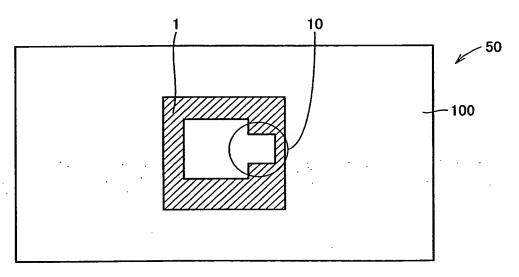


FIG.15

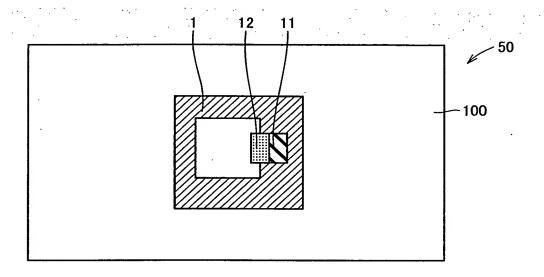


FIG.16

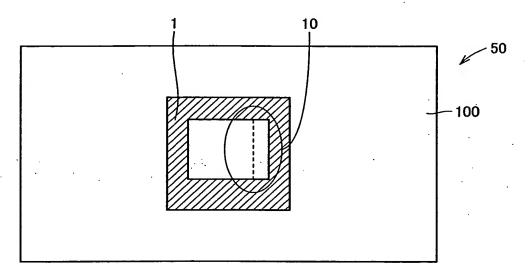


FIG.17

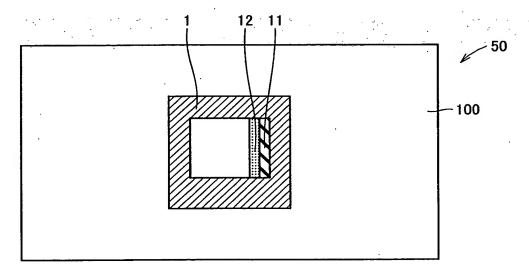


FIG.18

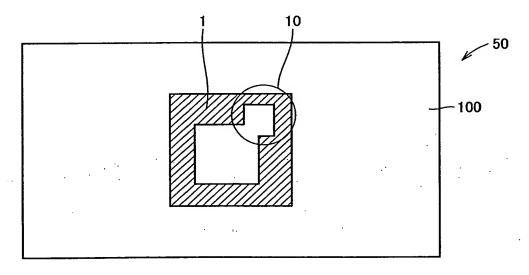
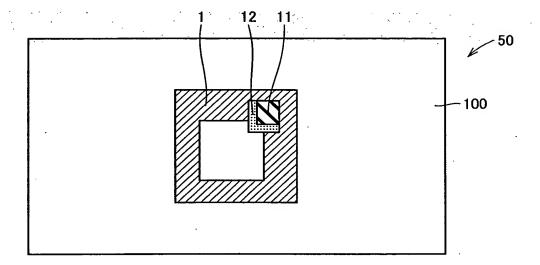
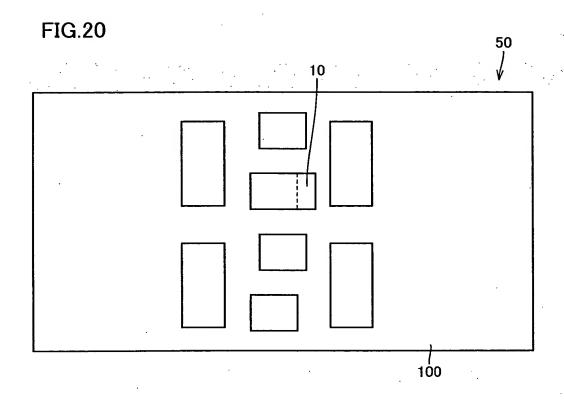
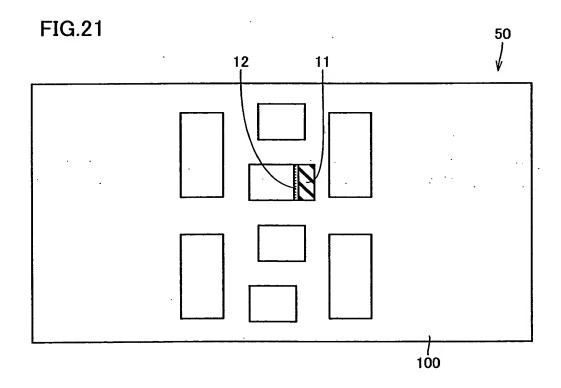


FIG.19







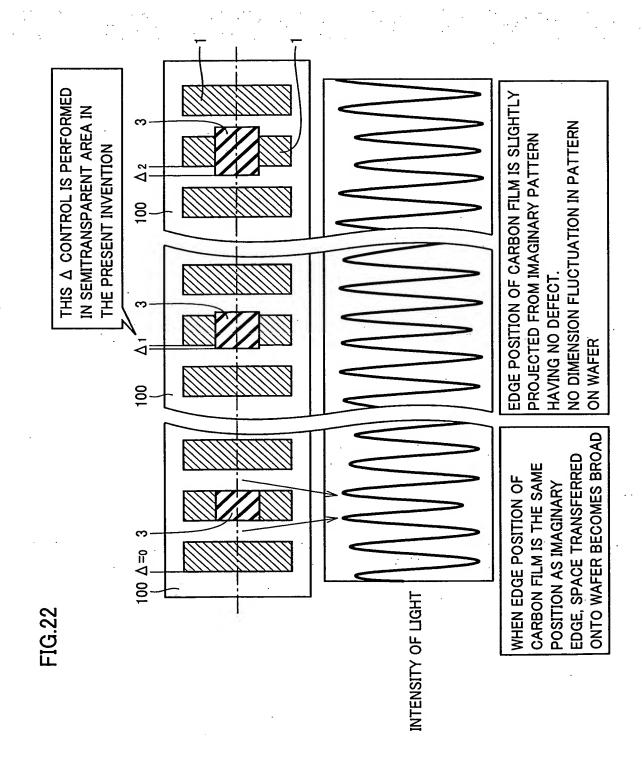


FIG.23

METHOD OF COMPARATIVE EMBODIMENT

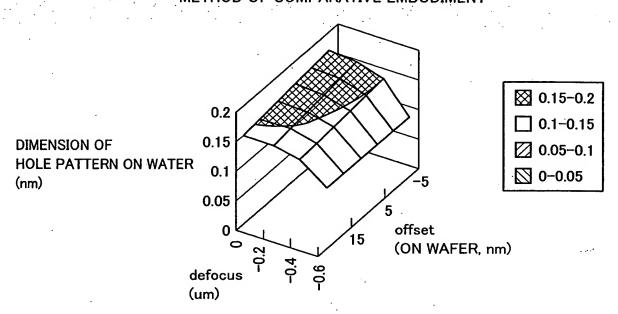
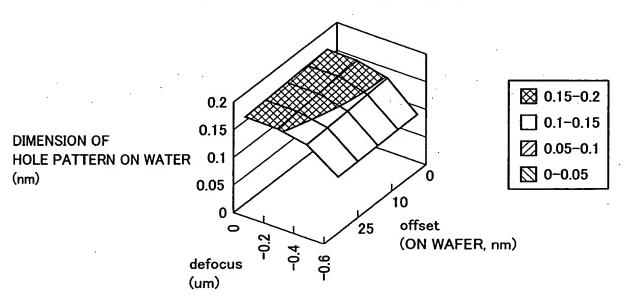


FIG.24

METHOD OF THE PRESENT INVENTION



-0.5Torr

0.2

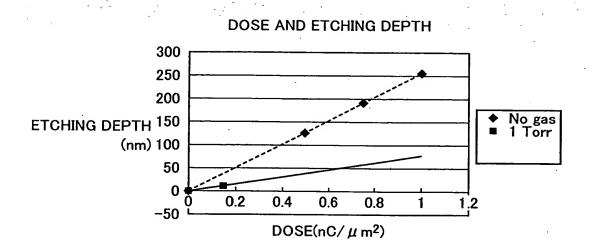


FIG.27

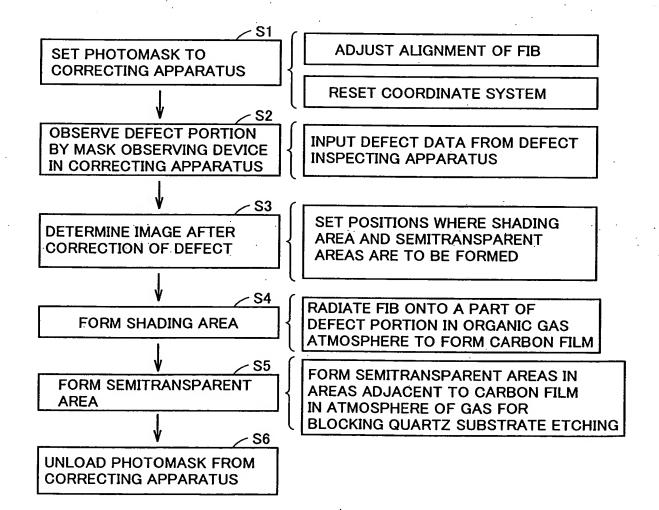


FIG.28

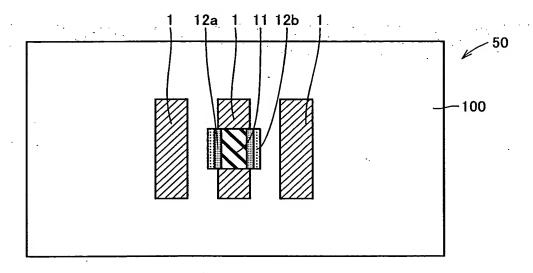


FIG.29

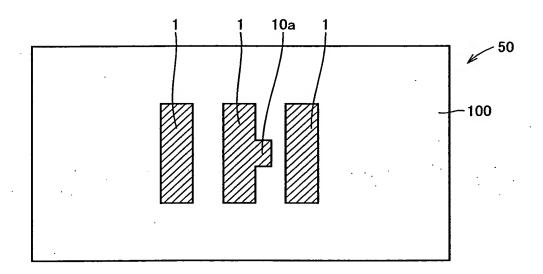


FIG.30

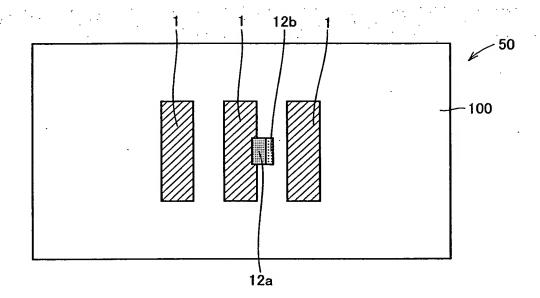
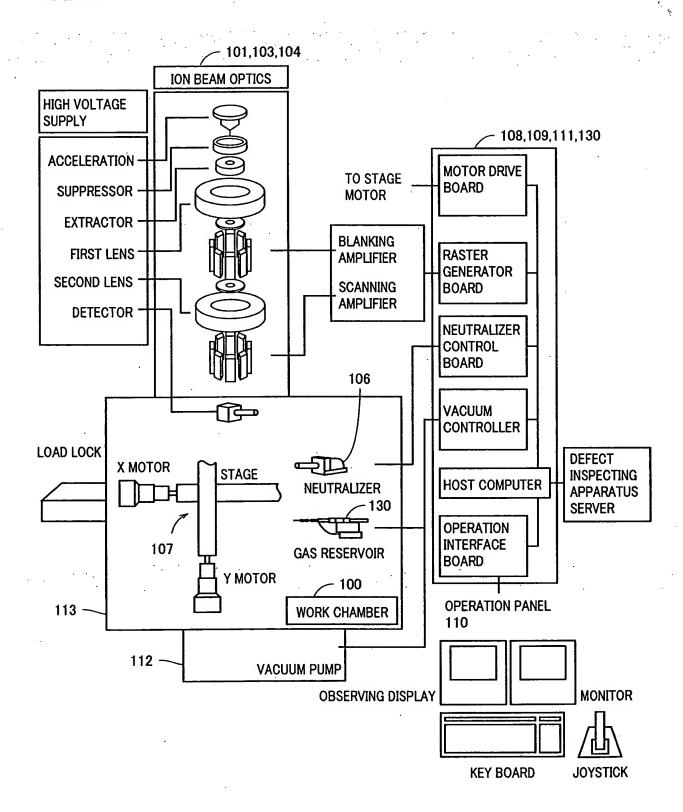
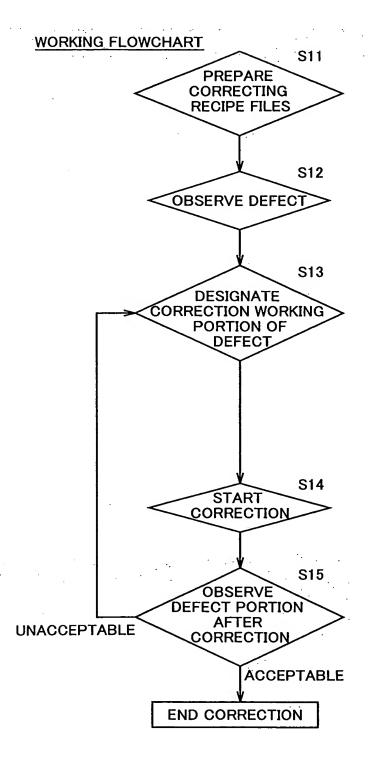


FIG.31





FLOWCHART OF MASK DEFECT CORRECTING APPARATUS OPERATION

